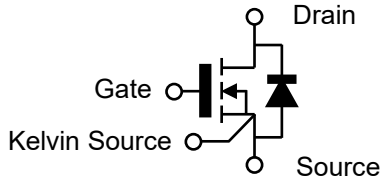


## Silicon Carbide MOSFET

650V, 65mΩ SiC MOSFET – Falcon Series



### Product Information:



TO-220-3L



TO-220FP-3L

### Features

- Optimized  $R_{DS(on)}$  with Rapid Switching Behavior
- Compatible with Standard Gate Drivers
- High Avalanche Endurance Capability
- Optimized for High Power Density Applications
- RoHS Compliant and Halogen Free

### Benefits

- Higher System Efficiency
- Increase Parallel Device Convenience
- Enable High Temperature Application
- Allow High Frequency Operation
- Realize Compact and Lightweight Systems
- High Reliability

Terminal	Packaging Type	
	TO-220-3L	TO-220FP-3L
Gate	1	1
Drain	2, Tab	2
Source	3	3
Kelvin Source	--	--

### Potential Applications

- Switching Mode Power Supply
- PFC & DC/DC Converter
- Portable Adaptor
- EV On Board Charger
- Renewable Energy
- Power Inverter & Motor Driver

### Key Performance Parameters

Parameter	Symbol	Value	Unit
Drain-Source Voltage	$V_{DS @ T_{j(max)}}$	700	V
Recommended Gate-Source Turn-On Voltage	$V_{GS}$	12~18	
Drain-Source On-State Resistance	$R_{DS(on)}$	65	mΩ
Continuous Drain Current	$I_D$	20	A
Pulse Drain Current	$I_{D, pulse}$		
Power Dissipation – TO-220-3L	$P_{tot}$	175	W
Power Dissipation – TO-220FP-3L	$P_{tot}$	65	
Avalanche Energy	$E_{AS}$	250	mJ
Gate Charge	$Q_G$	27.8	nC
Output Capacitive Charge	$Q_{oss}$	30.4	
Junction & Storage Temperature	$T_j, T_{stg}$	-55 to 175	°C

Part Number	Package	Marking
FF06080M2C	TO-220-3L	FF06080M2
FF06080M2D	TO-220FP-3L	FF06080M2

For further information about comparable products, please contact ([www.fastsic.com](http://www.fastsic.com)).

**Maximum Ratings: ( $T_j = 25^\circ\text{C}$ , unless otherwise specified)**

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Drain-Source Voltage	$V_{DSS}$	650	--	--	V	$V_{GS}=0\text{V}, I_D=100\mu\text{A}$
Continuous Drain Current	$I_D$	--	20 10	--	A	$V_{GS}=15\text{V}, T_c=25^\circ\text{C}, \text{TO-220-3L}$ $V_{GS}=15\text{V}, T_c=100^\circ\text{C}, \text{TO-220-3L}$
Continuous Drain Current	$I_D$	--	12 5	--		$V_{GS}=15\text{V}, T_c=25^\circ\text{C}, \text{TO-220FP-3L}$ $V_{GS}=15\text{V}, T_c=100^\circ\text{C}, \text{TO-220FP-3L}$
Pulse Drain Current	$I_{D,pulse}$	--	TBD	--		Per SOA
Continuous Body Diode Current	$I_S$	--	15	--		$V_{GS}=0\text{V}, T_c=25^\circ\text{C}$
Avalanche Energy, Single Pulse	$E_{AS}$	--	--	250		mJ
Operate Gate Source Voltage	$V_{GS,op}$	-8~0	--	12~18	V	Recommended operating values
Transient Gate Source Voltage	$V_{GS,tran.}$	-9	--	19		Transient operating limit ( $AC f > 1\text{Hz}$ , pulse width $< 100\text{ns}$ )
Power Dissipation	$P_{tot}$	--	175	--	W	$T_c=25^\circ\text{C}, \text{TO-220-3L}$
Power Dissipation	$P_{tot}$	--	65	--		$T_c=25^\circ\text{C}, \text{TO-220FP-3L}$
Junction Temperature	$T_j$	-55	--	175	°C	--
Storage Temperature	$T_{stg}$	-55	--	175		
Soldering Temperature	$T_L$	--	--	260		

**Electrical Characteristics:**

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions
<b>DC Characteristics (at <math>T_j = 25^\circ\text{C}</math>, unless otherwise specified)</b>						
Drain-source Breakdown Voltage	$V_{(BR)DSS}$	650	--	--	V	$V_{GS}=0\text{V}, I_D=1\text{mA}, T_j=25^\circ\text{C}$
Drain-Source On-State Resistance	$R_{DS(on)}$	--	94 75 65	--	mΩ	$V_{GS}=12\text{V}, I_D=8\text{A}, T_j=25^\circ\text{C}$ $V_{GS}=15\text{V}, I_D=8\text{A}, T_j=25^\circ\text{C}$ $V_{GS}=18\text{V}, I_D=8\text{A}, T_j=25^\circ\text{C}$
Gate-Source Threshold Voltage	$V_{th}$	--	2.5	--	V	$V_{GS}=V_{DS}, I_D=8\text{mA}$
Zero Gate Voltage Drain Current	$I_{DSS}$	--	1	1000	μA	$V_{DS}=650\text{V}, V_{GS}=0\text{V}, T_j=25^\circ\text{C}$
Gate-Source Leakage Current	$I_{GSS}$	--	--	100	nA	$V_{GS}=15\text{V}, V_{DS}=0\text{V}$
Body Diode Forward Voltage	$V_{SD}$	--	2.65 2.45	--	V	$V_{GS}=0\text{V}, I_S=4\text{A}, T_j=25^\circ\text{C}$ $V_{GS}=0\text{V}, I_S=4\text{A}, T_j=175^\circ\text{C}$
<b>AC Characteristics (at <math>T_j = 25^\circ\text{C}</math>, unless otherwise specified)</b>						
Input Capacitance	$C_{ISS}$	--	1030	--	pF	$V_{DS}=400\text{V}, V_{GS}=0\text{V},$ $f=250\text{kHz}, V_{AC}=25\text{mV}$
Output Capacitance	$C_{OSS}$	--	48	--		
Reverse Capacitance	$C_{RSS}$	--	3.2	--		
Effective Output Capacitance, energy related	$C_{o(er)}^1$	--	65	--		
Effective Output Capacitance, time related	$C_{o(tr)}^2$	--	85	--		
$C_{OSS}$ Stored Energy	$E_{OSS}$	--	4	--		
Output Capacitive Charge	$Q_{OSS}$	--	30.4	--	nC	
Internal Gate Resistance	$R_{G,int.}$	--	5.5	--	Ω	$f=1\text{MHz}, V_{AC}=25\text{mV}$

<sup>1</sup>  $C_{o(er)}$  is a fixed capacitance that gives the same stored energy as  $C_{OSS}$  while  $V_{DS}$  is rising from 0 to 400V.

<sup>2</sup>  $C_{o(tr)}$  is a fixed capacitance that gives the same charging time as  $C_{OSS}$  while  $V_{DS}$  is rising from 0 to 400V.

**Switching Characteristics:**

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions
<b>Gate Characteristics</b>						
Gate to Source Charge	$Q_{GS}$	--	7.4	--	nC	$V_{DS}=400V, V_{GS}=0V/15V, I_D=8A$
Gate to Drain Charge	$Q_{GD}$	--	8.3	--		
Total Gate Charge	$Q_G$	--	27.8	--		
Turn On Delay Time	$t_{d(on)}$	--		--	ns	$V_{DS}=400V,$ $I_D=8A,$ $V_{GS}=-3/+15V,$ $R_{G, ext.} = 4.7\Omega$ FWD=Self
Rise Time	$t_r$	--		--		
Turn Off Delay Time	$t_{d(off)}$	--		--		
Fall Time	$t_f$	--		--		
Turn On Switching Energy	$E_{on}$	--	40	--	$\mu J$	
Turn Off Switching Energy	$E_{off}$	--	6	--		
<b>Body Diode Characteristics</b>						
Reverse Recovery Charge	$Q_{rr}$	--	46.2	--	nC	$V_{GS}=0V,$ $I_S=8A, V_{DS}=400V,$ $di/dt=-A/\mu s$ * $Q_{rr}$ herein excluded the $Q_{oss}$ value.
Reverse Recovery Time	$t_{rr}$	--		--	ns	
Peak Reverse Recovery Current	$I_{rrm}$	--		--	A	

**Thermal Characteristics:**

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Thermal Impedance, junction-case	$R_{th-jc}$	--	0.85	--	K/W	TO-220-3L
Thermal Impedance, junction-case	$R_{th-jc}$	--	2.30	--		TO-220FP-3L
Thermal Impedance, junction-ambient	$R_{th-ja}$	--	40	--		Device on PCB, with 6 cm <sup>2</sup> of cooling area

## Revision History

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Date	Revision	Changes
2026.02	Tentative	First issue. All data is the target spec.

## Important Note (Disclaimer)

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